



IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Kenneth M. Williams et al.  
APPLICATION NO.: 10/630,542  
FILED: July 29, 2003  
TITLE: Defining Instruction Extensions in a Standard Programming Language  
EXAMINER: Isaac Tuku Tecklu  
GROUP ART UNIT: 2192  
ATTY.DKT.NO.: PA2221US

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Ron L. Rohde, Reg. No. 45,050

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT  
Under 37 C.F.R. §§ 1.56, and 1.97-1.98

SIR:

09/14/2007 MGEPRN1 00000057 10530542

02 FC:1006

180.00 0P

Pursuant to the provisions of 37 C.F.R. §§ 1.56 and 1.97-98 of the Rules of Practice in Patent Cases, enclosed herewith is form PTO-SB-08, listing several references.

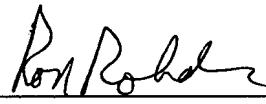
The Examiner is requested to make these references of official record in the application. The references cited may be material to examination of the application and are submitted in compliance with Applicant's duty of disclosure as defined by 37 C.F.R. § 1.56.

No representation is made or intended as to the completeness of this list, nor is the inclusion of any reference on this list an admission that it is prior art or pertinent to this application.

Applicant has enclosed the applicable fee of \$180.00 with this submission, in accordance to C.F.R. § 1.17(p). If more fees are due, however, the Commissioner is hereby authorized to charge any necessary fees to Account Number 06-0600. A copy of this statement is submitted in duplicate for this purpose.

Respectfully submitted,  
Kenneth M. Williams et al.

September 10, 2007

By:   
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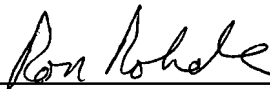
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**Complete if Known**

Application Number	10/630,542
Filing Date	July 29, 2003
First Named Inventor	Kenneth M. Williams
Art Unit	2192
Examiner Name	Isaac Tuku Tecklu
Attorney Docket Number	PA2221US

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> *Number <sup>4</sup> *Kind Code <sup>5</sup> (if known)				
		TW 478252	03-01-2002	Adaptive Silicon, Inc.		
		TW 480818	03-21-2002	Adaptive Silicon, Inc.		
		TW 509798	11-11-2002	Adaptive Silicon, Inc.		

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Sheet 2 of 2

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Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		PEDRO DINIZ, et al. "Automatic Synthesis of Data Storage and Control Structures for FPGA-based Computing Engines," 2000 IEEE, pages 91-100.	
		MICHAEL BEDFORD TAYLOR et al., "The Raw Microprocessor: A Computational Fabric for Software Circuits and General-Purpose Programs," March-April 2002, IEEE Micro, pages 25-35	
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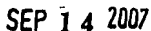
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